1. 21.

- II. Amend the following claim pursuant to 37 C.F.R. §1.121(a)(2)(i).In Claim 37, line 1, change "36" to --35-.
- III. Amend the following claims pursuant to 37 C.F.R. §1.121(a)(2)(ii).
 - (a) a first switch connected to a first input and having a first switch output, the first switch adapted to be selectively enabled for passing a first stream of data signals from the first input to the first switch output, the first stream of data signals including first channel data;

(Amended) An apparatus for receiving multiple data streams, the apparatus comprising:

- (b) a second switch connected to a second input and having a second switch output, the second switch adapted to be selectively enabled for passing a second stream of data signals from the second input to the second switch output, the second stream of data signals including second channel data different from the first channel data;
- (c) a data stream junction connected to the first switch output and the second switch output and having a junction output; [and]
- a controller for receiving a channel select input related to a desired channel output to be formed from one of the first channel data or second channel data, and, in response to the channel select input, for enabling the one of the first switch or the second switch which receives the stream of data including the channel data from which the desired channel output is to be formed[.]:and
 a memory device operatively connected to the controller, the memory device
 - storing first signal input information indicating the respective switch through

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	•			which the first chainer data is received and further storing second signal input
	2			information indicating the respective switch through which the second channel
· -	3			data is received.
	4			
	5	3. 23.	(Ame	nded) The apparatus of Claim $\frac{2}{2}$ [further including a memory device,] wherein
	6		the me	emory device [storing] also stores:
V	7		(a)	[first channel output information, the first channel output information including
	8			(i)] first signal processing information to control the processing of the first
	9			channel data [and (ii) first signal input information indicating the switch through
	10			which the first channel data is received]; and
	11		(b)	[second channel output information, the second channel output information
	12			including (i)] second signal processing information to control the processing of
	13			the second channel data [and (ii) second signal input information indicating the
	14			switch through which the second channel data is received].
	15			
b 3	16	28.	(Ame	nded) An apparatus for receiving multiple data streams, the apparatus comprising:
	17		(a)	a plurality of input paths, each respective input path for carrying a different data
	18			stream;
	19		(b)	a switching structure associated with the plurality of input paths for selectively
	20			blocking the respective data stream on each different input path; [and]
	21		(c)	a controller for receiving a channel select input related to a desired channel
	22			output to be formed from data included in one of the different data streams, and



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	1		for responding to the channel select input by blocking at least one of the
	2		plurality of data streams which does not include the channel data from which the
	3		desired channel output is to be formed[.]; and
	4		(d) a memory device operatively connected to the controller, the memory device
	5		storing channel output information for each different channel output which may
	6		be produced from the plurality of data streams, the channel output information
	7		for each respective channel output including signal input information indicating
	8		the input path on which the respective channel data is carried.
	9		
	10	10.30.	(Amended) The apparatus of Claim 29 [further comprising a memory device for storing
B	11		channel output information for each different channel output which may be produced
	12		from the plurality of data streams,] wherein the channel output information for each
	13		respective channel output <u>further includes</u> [including] signal processing information to
	14		control the signal processor in processing the respective channel data[, and signal input
	15		information indicating the input path on which the respective channel data is carried].
	16		
	17	15.35.	(Amended) A method for receiving multiple data streams, the method comprising the
	18		steps of:
	19		(a) directing a plurality of different data streams each along a different input path to
	20		a signal processor, each the data stream including channel data for producing a
	21		respective channel output;

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1	(b)	receiving a channel select input related to a desired channel output comprising a
2		particular one of the channel outputs; [and]
3	(c)	responding to the channel select input by blocking at least one of the data
4		streams which does not include channel data from which the desired channel
5		output is to be produced[.]; and
6	<u>(d)</u>	storing channel output information for each different channel output which may
7		be produced from the plurality of data streams, the channel output information
8		for each respective channel output including signal processing information to
9		control the signal processor in processing the respective channel data, and signa
10		input information indicating the input path on which the respective channel data
11		is carried.

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REMARKS

The Applicants respectfully request reconsideration and allowance of Claims 21 through 35 and 37 through 40 in light of the above amendments and the arguments set forth below.

The Applicants appreciate the Examiner's indication in the Final Office Action that Claims 23-27, 30-34, and 36-40 were directed to allowable subject matter.

I. The Amendments

Rejected independent Claim 21 is amended above to incorporate limitations from Claim 23 and Claim 23 is amended to remove the material added to Claim 21. Similarly, independent Claim 28 is amended above to incorporate limitations from Claim 30 and Claim 30 is amended

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